

## **AMENDMENTS TO THE SPECIFICATION:**

### **IN THE ABSTRACT:**

Please replace the original page 17 with the accompanying replacement page 17, which includes the following rewritten paragraph beginning at line 2:

1           A comparator with hysteresis which achieves fast switching despite a low  
2 current consumption. The comparator includes ~~comprises~~ a first transistor (M1)  
3 and a second transistor (M2) whose gates form the inputs of the comparator.  
4 The main current paths of both transistors are connected to each other at one  
5 end, with a third transistor (M3) and a fourth transistor (M4) being provided. The  
6 gate of the third transistor is connected to the gate of the first transistor and its  
7 main current path is circuited between the one end of the main current paths of  
8 the first and second transistor and is connected via the main current path of the  
9 fourth transistor to the other end of the main current path of the second  
10 transistor. The gate of the fourth transistor is connected to the output signal or  
11 inverted output signal of the comparator. The comparator in accordance with the  
12 invention may be put to use e.g. in an ASK demodulator such as those used in  
13 RFID transponders.